

**AMENDMENTS TO THE CLAIMS:**

This listing of the claims will replace all prior versions, and listings, of the claims in this application.

**Listing of Claims:**

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1. (Original) A method for configuring a computer network that includes a full duplex bi-directional first port and an initiator that can issue a request for said first port to assume a state,

wherein said first port, when in a first state, is able to bi-directionally communicate with a full duplex bi-directional second port,

wherein said first port, when in a second state, is coupled to itself by having an output thereof coupled to an input thereof,

said method comprising:

inhibiting said initiator from issuing said request,

sending data to said initiator describing a desired state of said first port, and

enabling said initiator to issue said request for said first port to assume said desired state.

2. (Currently Amended) The method of claim 1, further comprising after ~~said~~ inhibiting step, the ~~step of~~ receiving data from said initiator describing an actual state of said first port.

3. (Currently Amended) The method of claim 2, further comprising after ~~said~~ receiving step, the ~~step of~~ determining said desired state based on said actual state.

4. (Original) The method of claim 1, wherein said computer network includes a plurality of webs, and each of said plurality of webs includes a respective initiator and a respective full duplex bi-directional port.

5. (Currently Amended) The method of claim 1, wherein prior to ~~said~~ inhibiting ~~step~~ said computer network has M number of webs, and each of said M number of webs includes a respective initiator and a respective full duplex bi-directional port, and after ~~said~~ enabling ~~step~~ said computer network has N number of webs, where N is not equal to M.

6. (Original) The method of claim 1, wherein said initiator is one of a plurality of initiators, and wherein ~~said~~ inhibiting ~~step~~ comprises inhibiting said plurality of initiators from issuing said request, and wherein ~~said~~ enabling ~~step~~ comprises enabling said plurality of initiators in a determined sequence.

7. (Original) The method of claim 1, wherein said desired state is specified by a user.

8. (Original) The method of claim 1, wherein said computer network conforms to American National Standards Institute (ANSI) Standard X3T10.1.

9. (Original) A controller for configuring a computer network that includes a full duplex bi-directional first port and an initiator that can issue a request for said first port to assume a state,

wherein said first port, when in a first state, is able to bi-directionally communicate with a full duplex bi-directional second port,

wherein said first port, when in a second state, is coupled to itself by having an output thereof coupled to an input thereof,

said controller comprising:

means for inhibiting said initiator from issuing said request,

means for sending data to said initiator describing a desired state of said first port, and

means for enabling said initiator to issue said request for said first port to assume said desired state.

10. (Original) The controller of claim 9, further comprising means for receiving data from said initiator describing an actual state of said first port.

11. (Original) The controller of claim 10, further comprising means for determining said desired state based on said actual state.

12. (Original) The controller of claim 9, wherein said computer network includes a plurality of webs, and each of said plurality of webs includes a respective initiator and a respective full duplex bi-directional port.

13. (Original) The controller of claim 9, wherein said computer network has M number of webs, and each of said M number of webs includes a respective initiator and a respective full duplex bi-directional port, and said controller configures said computer network to yield N number of webs, where N is not equal to M.

14. (Original) The controller of claim 9, wherein said initiator is one of a plurality of initiators, and wherein said inhibiting means inhibits said plurality of initiators from issuing said request, and wherein said enabling means enables said plurality of initiators in a determined sequence.

15. (Original) A storage media including instructions for controlling a processor that, in turn, configures a computer network that includes a full duplex bi-directional first port and an initiator that can issue a request for said first port to assume a state,

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wherein said first port, when in a first state, is able to bi-directionally communicate with a full duplex bi-directional second port,

wherein said first port, when in a second state, is coupled to itself by having an output thereof coupled to an input thereof,

said storage media comprising:

means for controlling said processor to inhibit said initiator from issuing said request,

means for controlling said processor to send data to said initiator describing a desired state of said first port, and

means for controlling said processor to enable said initiator to issue said request for said first port to assume said desired state.

16. (Original) The storage media of claim 15, further comprising means for controlling said processor to receive data from said initiator describing an actual state of said first port.

17. (Original) The storage media of claim 16, further comprising means for controlling said processor to determine said desired state based on said actual state.

18. (Original) The storage media of claim 15, wherein said computer network includes a plurality of webs, and each of said plurality of webs includes a respective initiator and a respective full duplex bi-directional port.

19. (Currently Amended) The storage media of claim ~~15~~ 15, wherein said computer network has M number of webs, and each of said M number of webs includes a respective initiator and a respective full duplex bi-directional port, and said processor configures said computer network to yield N number of webs, where N is not equal to M.

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20. (Original) The storage media of claim 15, wherein said initiator is one of a plurality of initiators, and wherein said storage media comprises means for controlling said processor to inhibit said plurality of initiators from issuing said request, and means for controlling said processor to enable said plurality of initiators in a determined sequence.

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